Singola: DCM3717 Customer Evaluation Board

REV	DESCRIPTION	DATE	APPROVED
	SEE SHEET 1		

SHEET#	DESCRIPTION
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3	Enable Control, PMBus Interface
4	DCM - Fusing, Filter, Control
5	Optional +INV discharge
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RAIL	Chipset	Vin [V]	Icont [A]	Ipeak [A]	Vout [V]	Pcont [W]	Ppeak [W]	Fuse
P12V2	DCM3717S60E13G5TN1	40-60	61.5	74	10-12.5	750	900	30A
P12V2	DCM3717S60D13K0TN1	40-60	82.0	90.2	10-12.5	1000	1100	40A

TOP-LAYER	BOTTOM-LAYER
FID01 FIDUCIAL	FID04 FIDUCIAL
FID02 FIDUCIAL	FID05 FIDUCIAL
FID03 FIDUCIAL	FID06 FIDUCIAL

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VICOR

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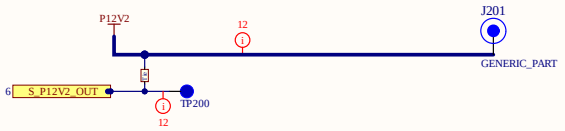
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FILENAME: 52841_1R#1.SchDoc				
DRAWN: Rosselli	DATE: 3/20/2025	SCALE: 1:1	SHEET 1 OF 6	

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System power connections

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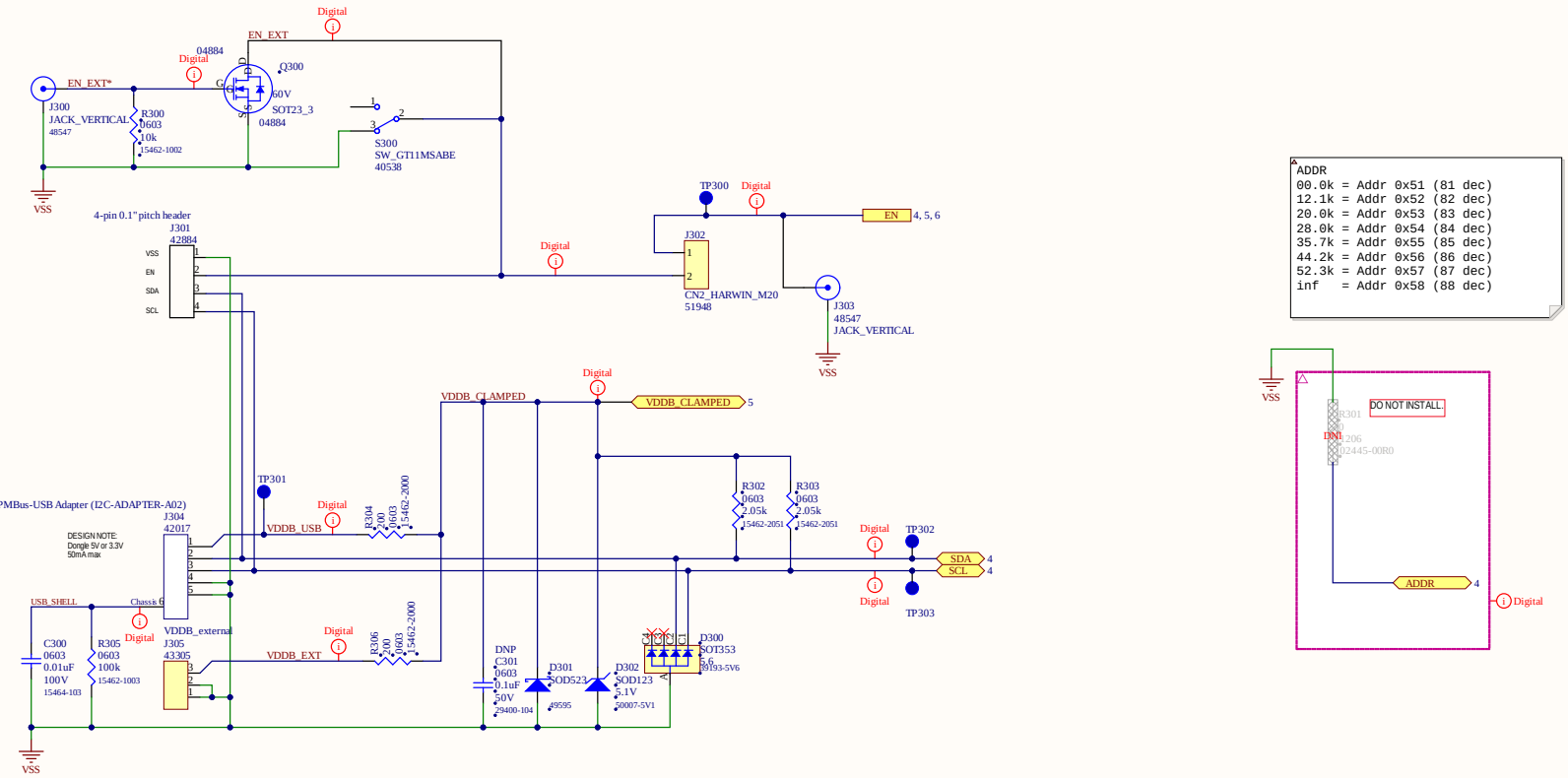
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FILENAME: 52841_2R#1.SchDoc				
DRAWN: Rosselli	DATE: 3/20/2025	SCALE: 1:1	SHEET 2 OF 6	

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Enable control, PMBus interface

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```
ADDR
00.0k = Addr 0x51 (81 dec)
12.1k = Addr 0x52 (82 dec)
20.0k = Addr 0x53 (83 dec)
28.0k = Addr 0x54 (84 dec)
35.7k = Addr 0x55 (85 dec)
44.2k = Addr 0x56 (86 dec)
52.3k = Addr 0x57 (87 dec)
inf   = Addr 0x58 (88 dec)
```


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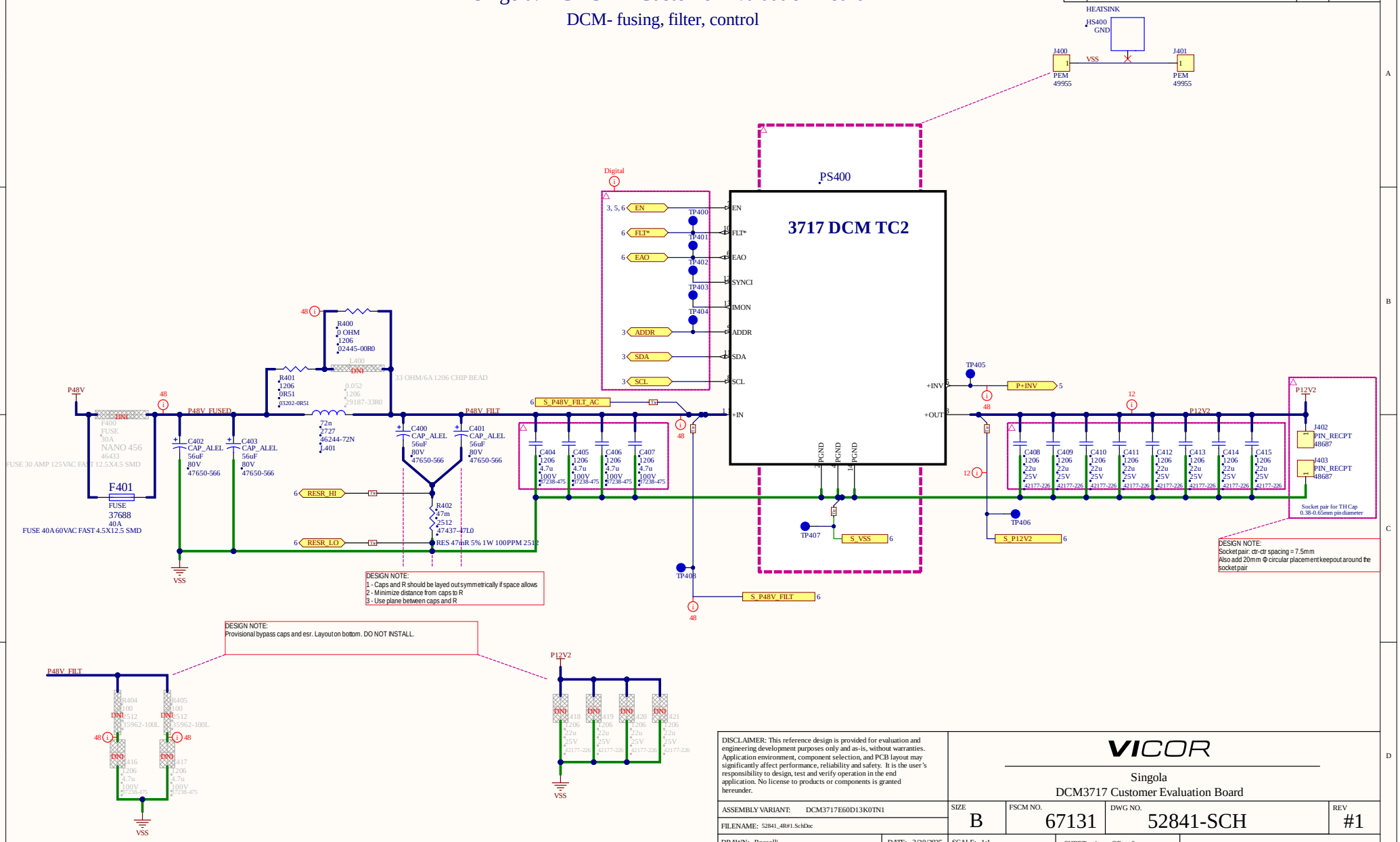
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FILENAME: 52841_3R#1.SchDoc				
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DCM- fusing, filter, control

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VICOR

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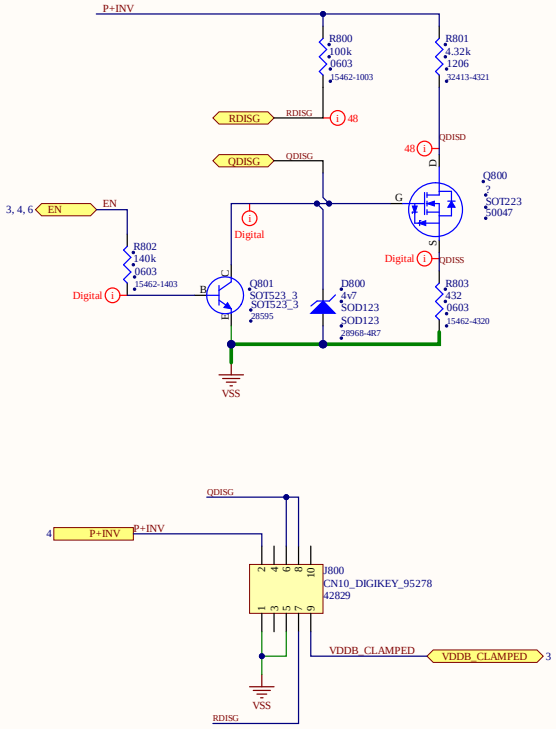
DCM3717 Customer Evaluation Board

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FILENAME: 52841_4R#1.SchDoc			
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OPTIONAL: +INV discharge circuit, reduces delay needed for monotonic restart

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VICOR

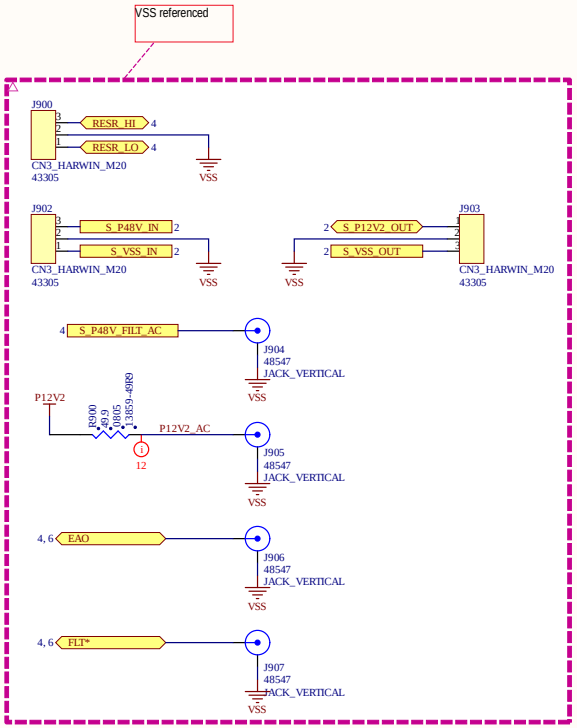
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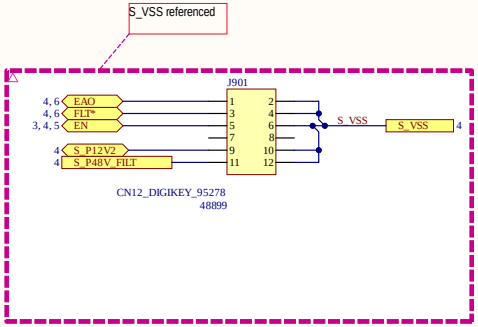
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FILENAME: 52841_5R#1.SchDoc				
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System testpoints

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ASSEMBLY VARIANT: DCM3717E60D13K0TN1	SIZE B	FSCM NO. 67131	DWG NO. 52841-SCH	REV #1
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DRAWN: Rosselli	DATE: 3/20/2025	SCALE: 1:1	SHEET 6 OF 6	