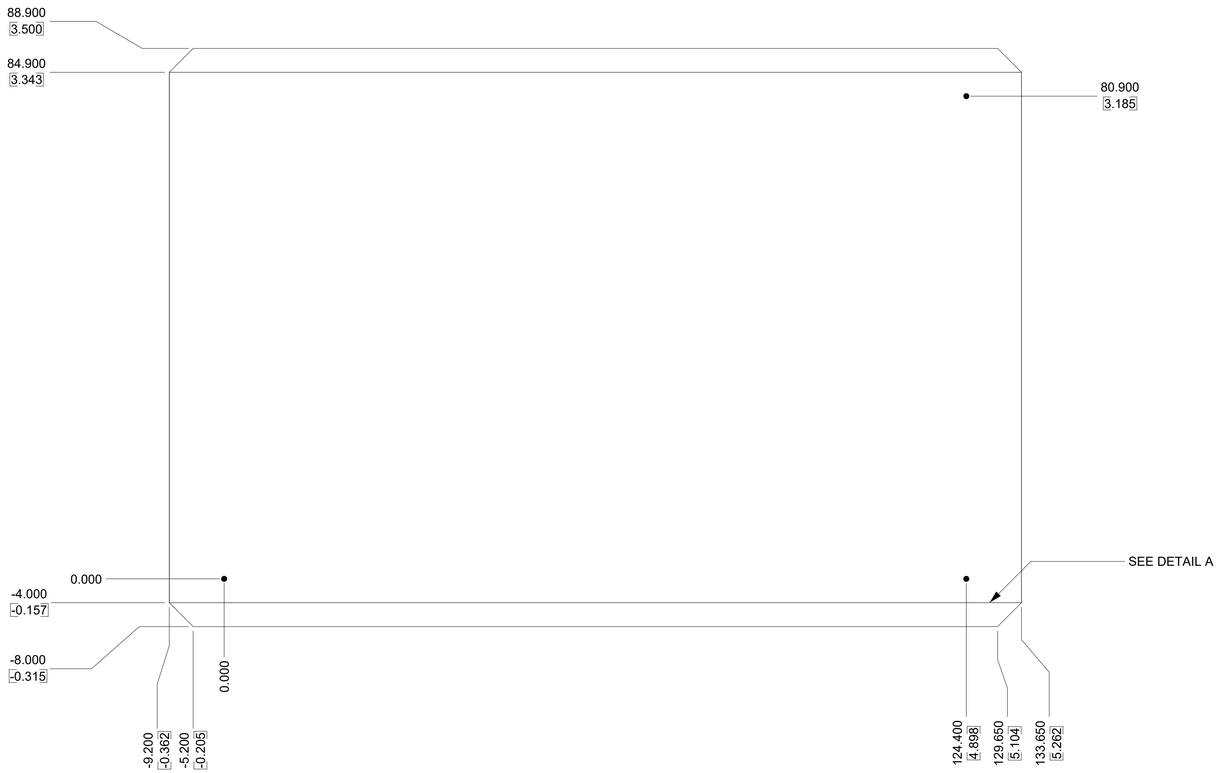
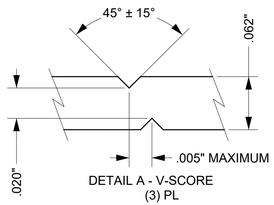
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REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
01	Preliminary for engineering GEE	02/11/25	SF





DETAIL A - V-SCORE
(3) PL
SCALE: NONE

NOTES:
1- DIMENSIONS ARE MM/[inch].
2- [ ] DENOTES REFERENCE DIMENSION AND TOLERANCE ONLY AND SHOULD NOT BE USED FOR PROGRAMMING OR INSPECTION PURPOSES.
3- <> DENOTES CRITICAL CHARACTERISTIC FOR LOT INSPECTION.
4- POSITIONAL TOLERANCES TO FEATURES WITHIN THE CIRCUIT ARE NON-CUMULATIVE. UNLESS OTHERWISE SPECIFIED THE FOLLOWING TOLERANCES APPLY:
   A. ARRAY DATUM TO SLOTS ±0.203mm [0.008in]
   B. ARRAY DATUM TO HOLES ±0.152mm [0.006in]
   C. ARRAY DATUM TO COPPER ±0.127mm [0.005in]

DRAWN	DATE
D.LeBLANC	02/11/25

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE MM [INCH]
TOLERANCES ARE:
DECIMALS ANGLES
X.X [X.XX] = ±0.25 [0.01] ±1°
X.XX [X.XX] = ±0.25 [0.01]
X.XX [X.XXX] = ±0.127 [0.005]
X.XXX [X.XXX] = ±0.127 [0.005]
X.XXX [X.XXXX] = ±0.0127 [0.0005]
X.XXXX [X.XXXXX] = ±0.0127 [0.0005]

THIRD ANGLE PROJECTION
DO NOT SCALE DRAWING

VICOR

FAB PCB 55032

SIZE	CAGE CODE	DWG NO	REV
D	67131	55032-FAB	01

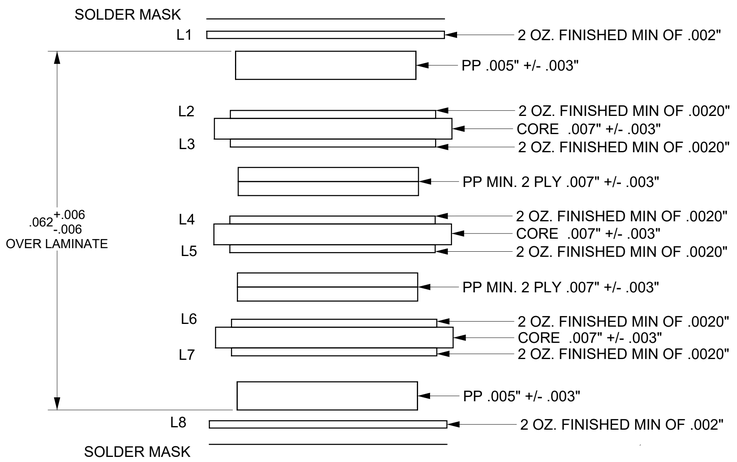
SCALE 2 : 1 | SHEET 1 OF 2

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REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
	SEE SHEET 1		

8 LAYER DETAIL

FINISHED COPPER WEIGHT AND MATERIAL



Quantity	Start Layer	End Layer	Size (mm)	Tolerance (mm)	Plated	Notes
956	1	8	0.254	+0.1/-0.76	Yes	
363	1	8	0.508	+0.1/-0.76	Yes	
48	1	8	0.762	+/-0.076	Yes	
4	1	8	3.400	+/-0.076	Yes	
4	1	8	3.400	+/-0.0508	No	
6	1	8	7.010	+/-0.076	Yes	
Total		1381				

NOTES:

1. REQUIREMENTS.
   A.) BOARD TO BE FABRICATED PER IPC-6012 CLASS 2 (CURRENT REVISION) EXCEPT WHERE NOTED.
   B.) BOARD TO BE ACCEPTABLE PER IPC-A-600 (CURRENT REVISION).
   C.) THE SUPPLIER SHALL NOT MODIFY ANY PART OF THE DESIGN WITHOUT WRITTEN PERMISSION FROM VICOR.
   D.) THE SUPPLIER MAY NOT REMOVE NON-FUNCTIONAL PADS ON THE INNER-LAYERS.

2. LAMINATE SHALL BE EITHER TUC TU-862 OR 865, EMC-370D OR Z, ISOLA 370HR, NANYA NPG-170, VENTEC VT-47, OR ITEQ IT-180A, WHICH ARE ALL FR-4 WITH Tg >165C AND MOT RATING OF 130C. THE SUPPLIER SHALL BE 94v-0 UL APPROVED FOR THIS MATERIAL. THE SUPPLIER SHALL NOT USE ALTERNATIVE MATERIALS WITHOUT WRITTEN PERMISSION FROM VICOR.

3. FABRICATOR SHALL MARK THE FOLLOWING IN NON-CONDUCTIVE INK ON TOP OF SOLDERMASK:
   A.) VENDOR I.D.
   B.) U/L LOGO AND RATING
   C.) DATE CODE

4. SURFACE FINISH:
   A.) SOLDERMASK SHALL BE BLUE MATTE AND OVER BARE Cu (SMOBC).
   B.) VIAS WITH NO SOLDERMASK OPENING MAY BE ENCROACHED BY .003".
   C.) THE REMAINING EXPOSED SURFACES SHALL BE FINISHED WITH 2-5 MICROINCHES Au OVER 100-250 MICROINCHES Ni (ENIG).

5. SILKSCREEN BOTH SIDES WITH WHITE NON-CONDUCTIVE INK.

6. THE PREVIOUS REVISION OF THIS BOARD is 53795.

DRAWN	DATE
GEE	02/11/25

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE MM [INCH]
TOLERANCES ARE:
DECIMALS ANGLES
X.X [X.XX] = ±0.25 [0.01] ±1°
X.XX [X.XX] = ±0.25 [0.01]
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X.XXXX [X.XXXX] = ±0.0127 [0.0005]

THIRD ANGLE PROJECTION
DO NOT SCALE DRAWING

VICOR

FAB PCB 55032

SIZE	CAGE CODE	DWG NO	REV
D	67131	55032-FAB	01

SCALE 1 : 1 SHEET 2 OF 2